Digital Electronics Laboratory

- **List of Experiments**:

1. Study of Basic Gates and Universal Gates
2. Study of Universal Gates
3. Half Adder and full Adder
4. 4- Bit Parallel Adder and Subtractor
5. Code Conversion
6. Study of Magnitude Comparator
7. To study Truth table of MUX ICs 74157(Multiplexer QUAD 2:1), IC74150 (Multiplexer 16: 1) IC-74150
8. Study of Flip-Flop and their truth tables: SR, JK, D and T type
9. Study of Asynchronous counter
10. Study of Shift register
11. Introduction to VHDL
12. Study of CPLD and FPGA

- **List of Equipments**:

1. Logic Gate ICs: 7400,7402,7404,7408,7432,7486,7474,7476,7485,7495,7411
2. Clock pulse generator
3. DC Power supply
4. Bread board
5. Connecting wires
6. LEDs
7. XILINK 9.1i Simulator
8. PCs with windows-XP